



Docket No.: M4065.0771/P771  
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:  
Roger Panicacci

Application No.: 09/281,358

Confirmation Number: 7144

Filed: March 30, 1999

Art Unit: 2615

For: ANALOG TO DIGITAL CONVERTER  
WITH INTERNAL DATA STORAGE

Examiner: N. Tran

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Technology Center 2600

Dear Sir:

Submitted herewith is one set (twenty sheets, twenty figures) of formal drawings for filing in the above-identified patent application. Kindly substitute the enclosed formal drawings for the informal drawings submitted with the originally filed application.

Dated: September 3, 2003

Respectfully submitted,

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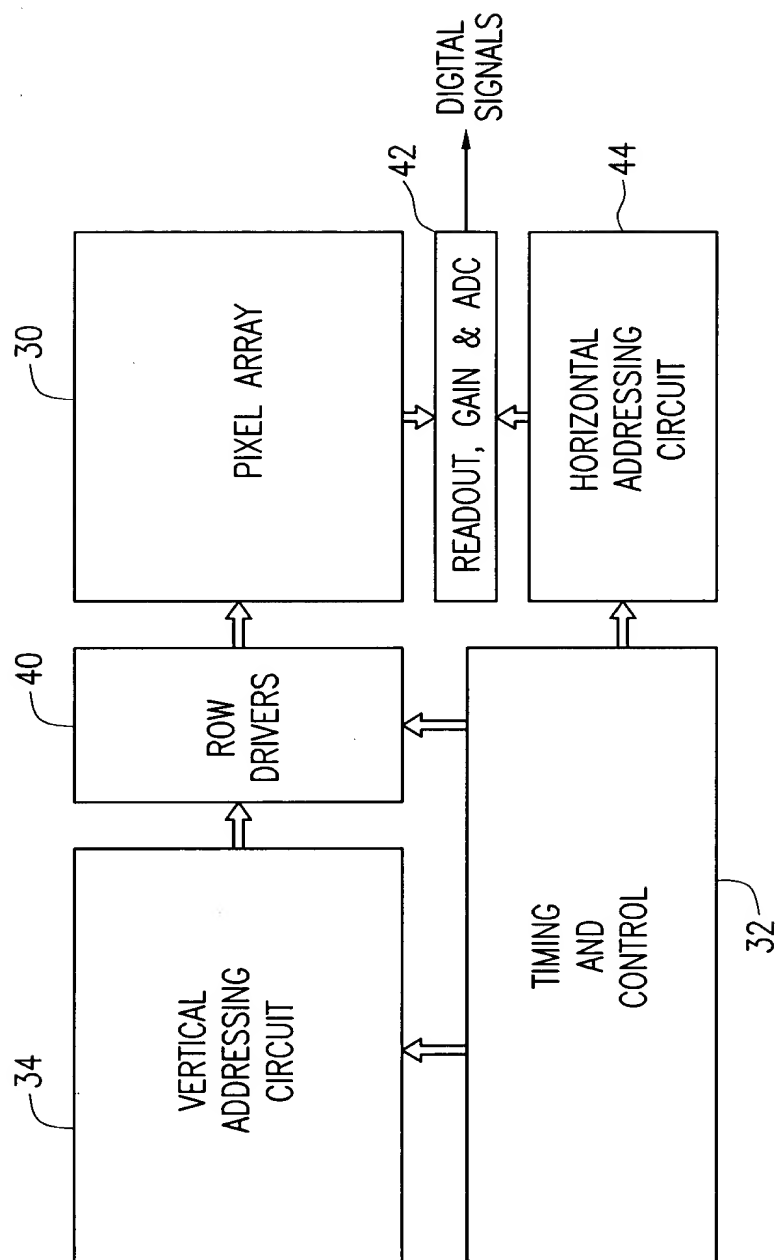


FIG. 1

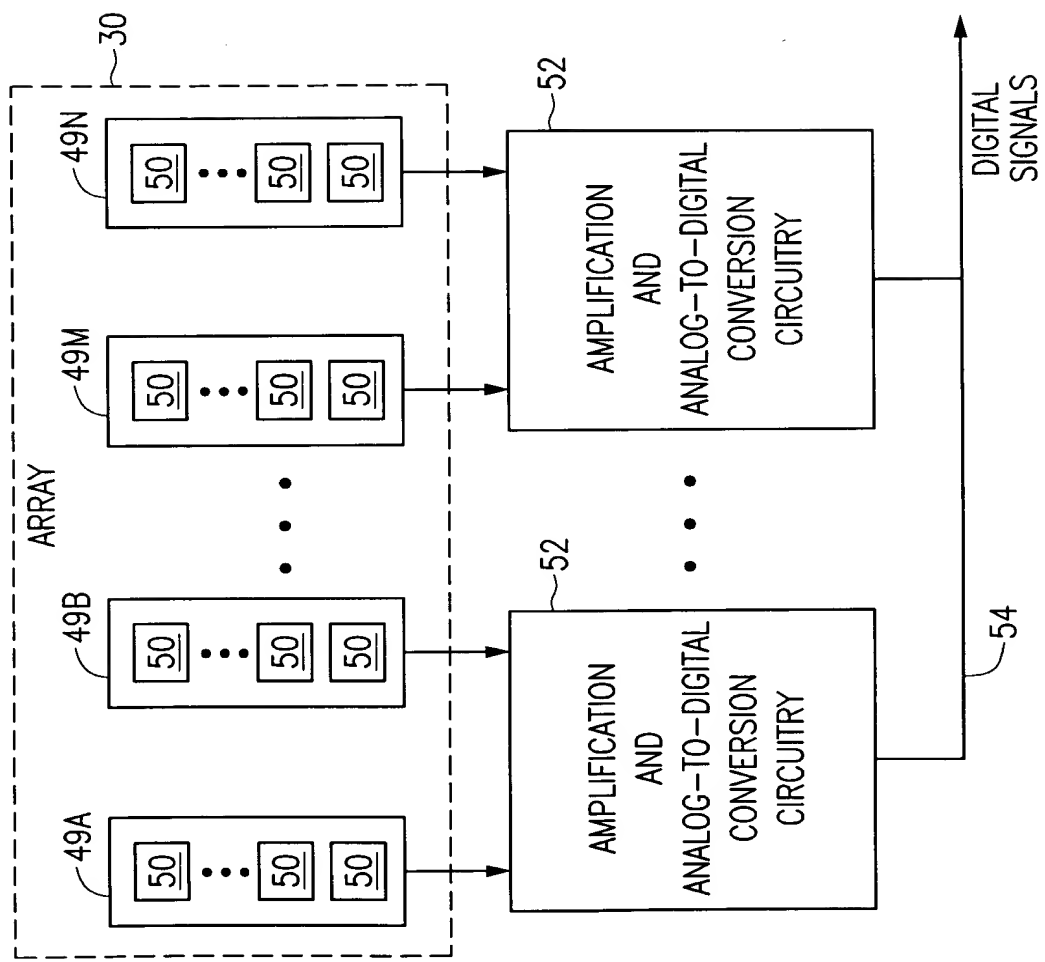


FIG. 2

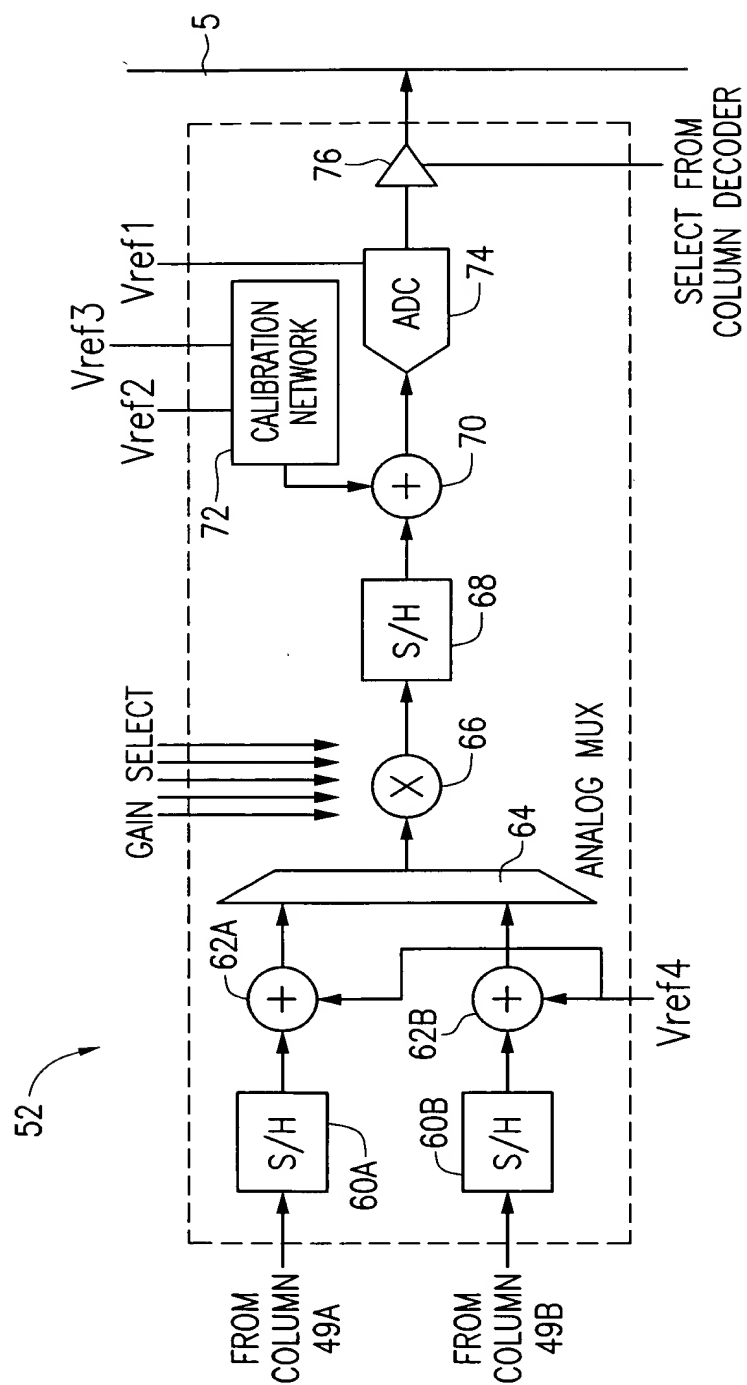


FIG. 3

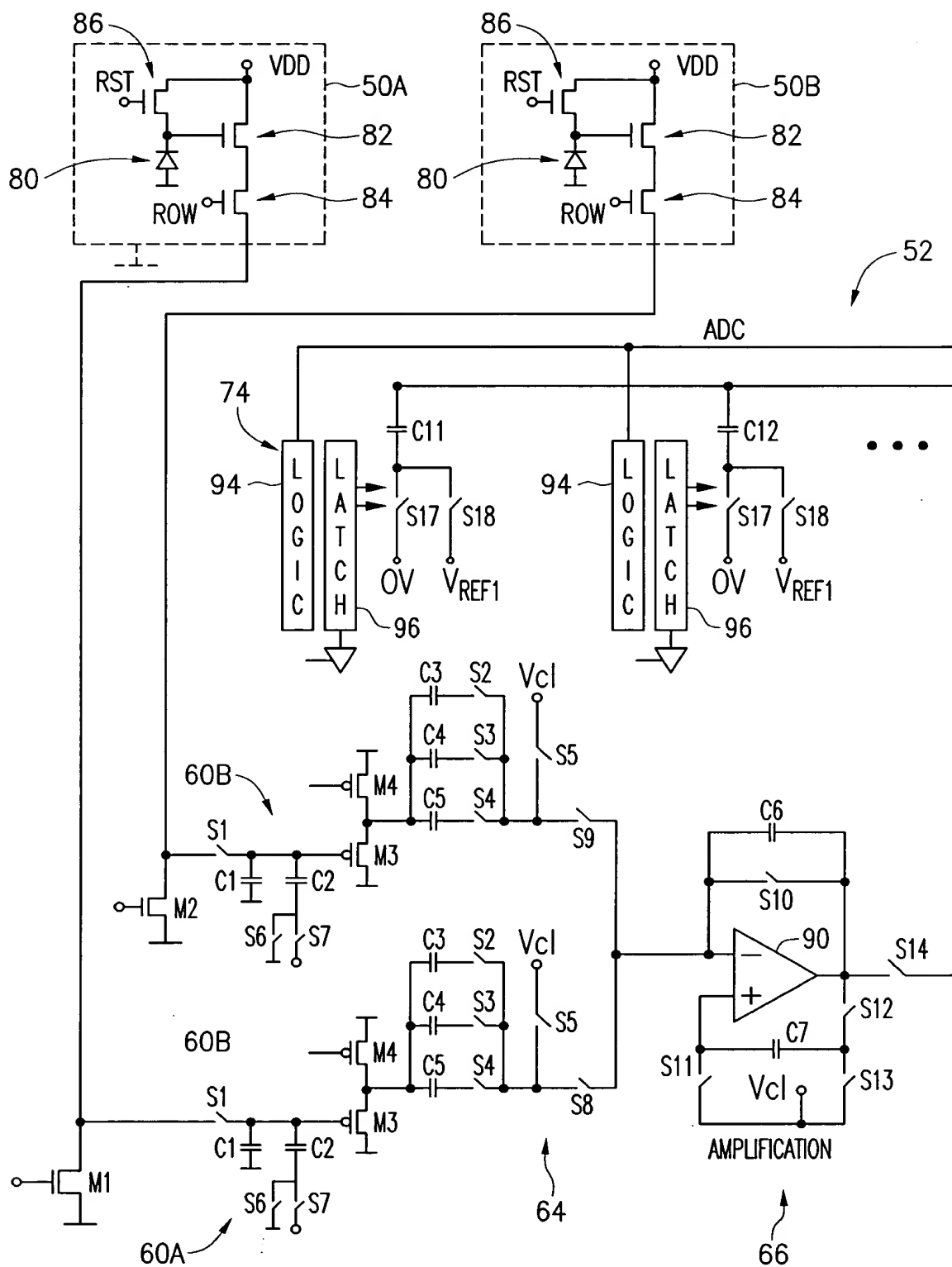


FIG. 4

FIG. 4A    FIG. 4B

FIG. 4A

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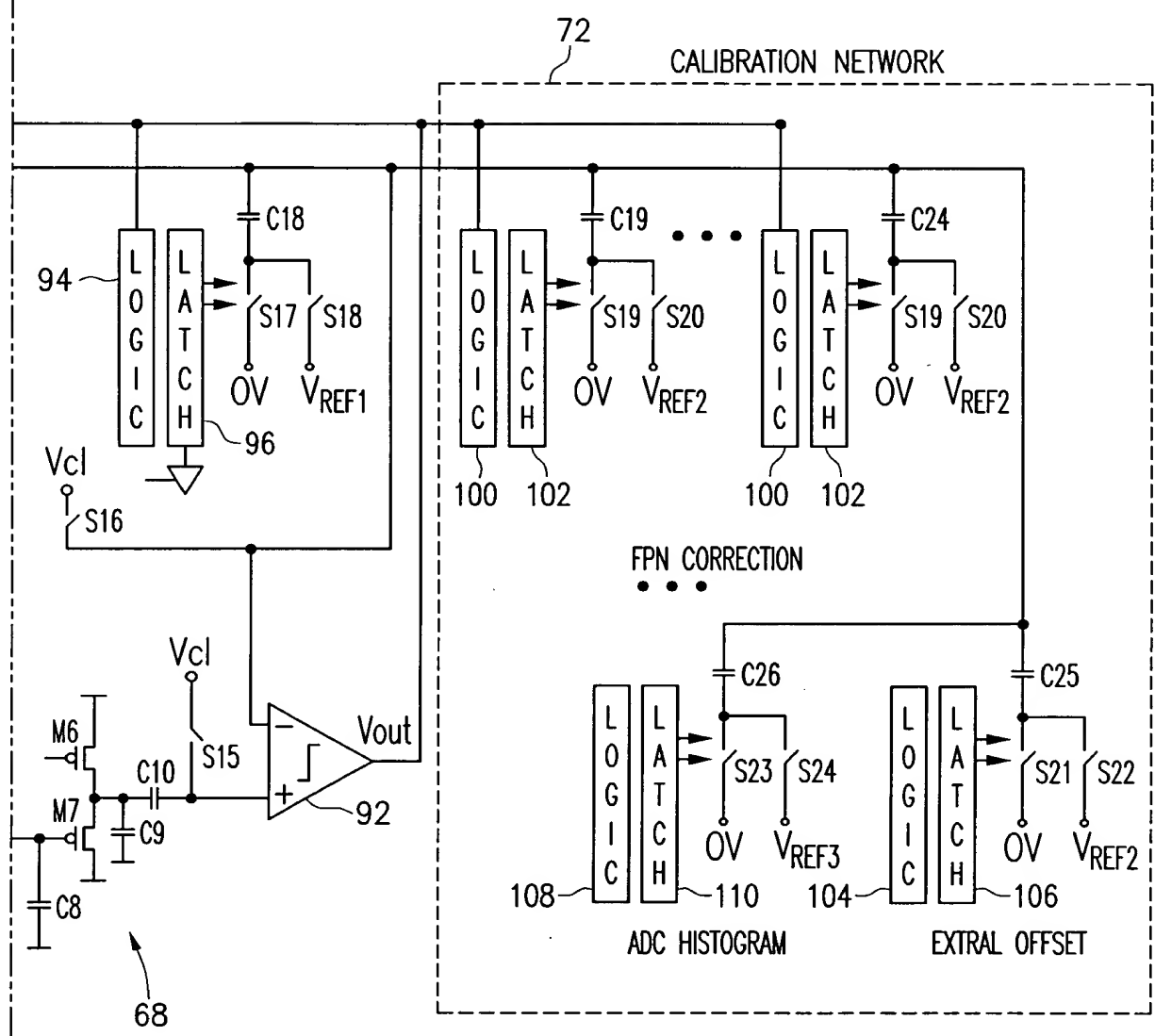
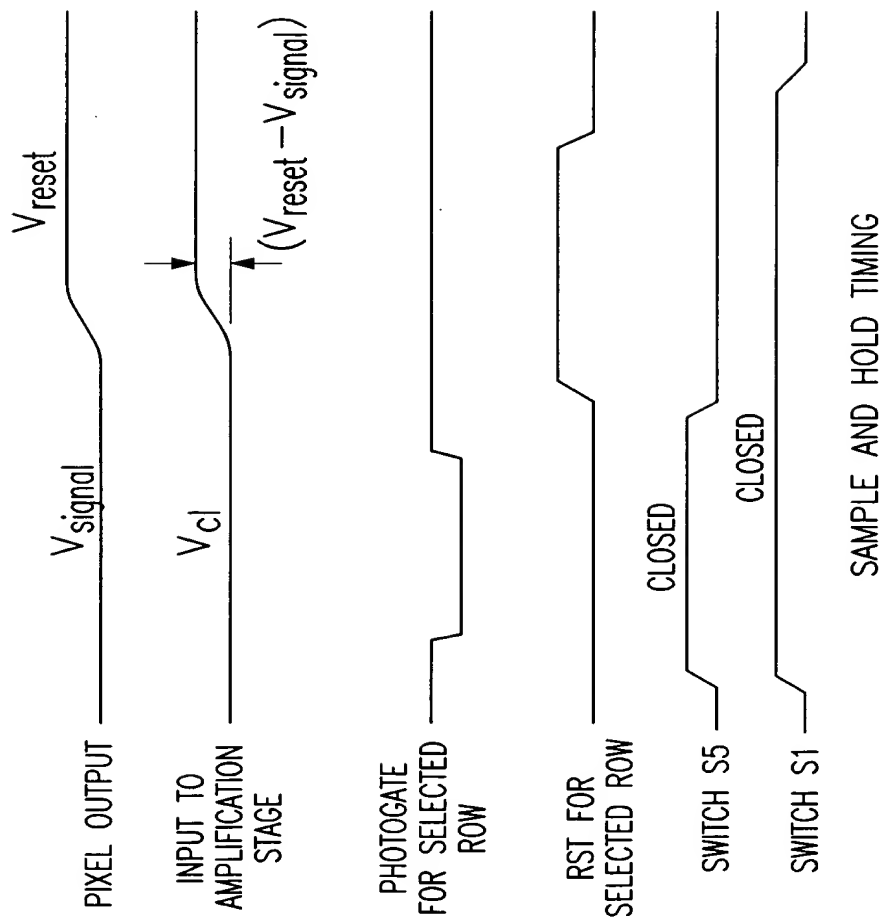
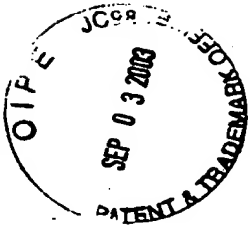


FIG. 4

FIG. 4A    FIG. 4B

FIG. 4B

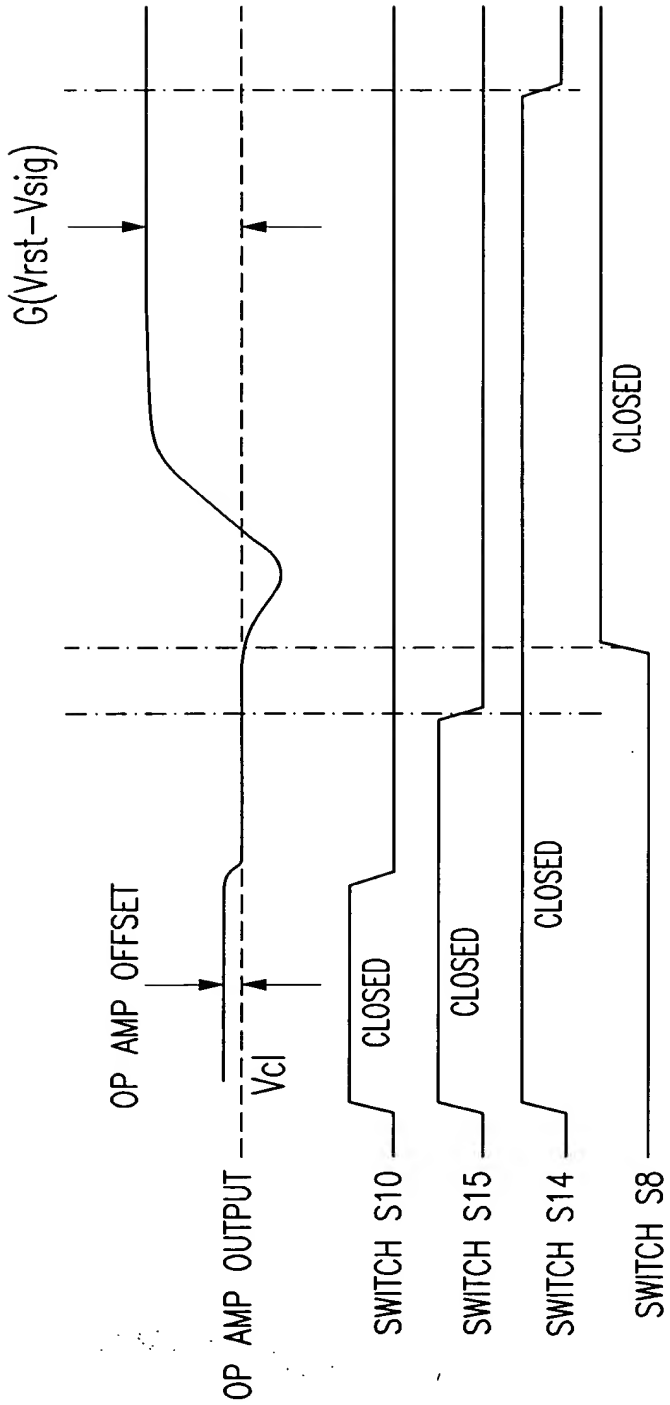
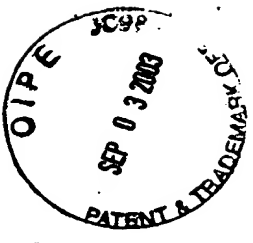


**FIG. 5**



**FIG. 6**





AMPLIFICATION TIMING

FIG. 7



**FIG. 8**

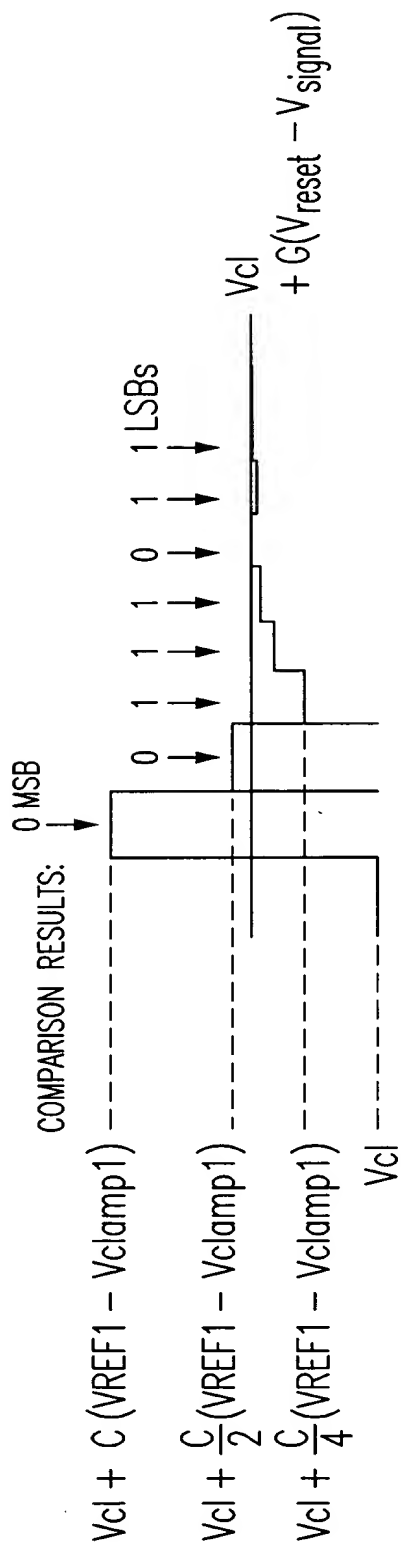


FIG. 9

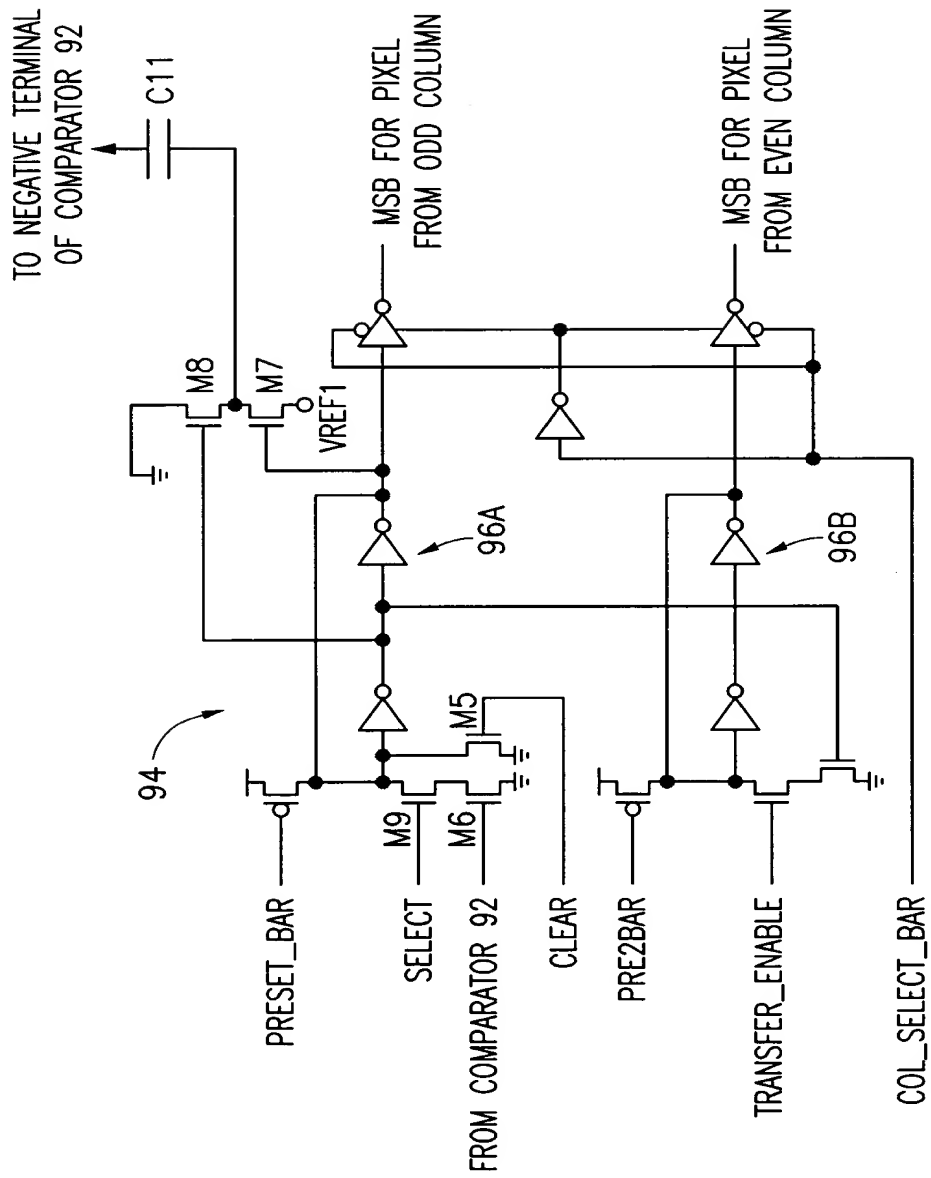
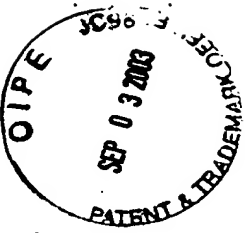


FIG. 10

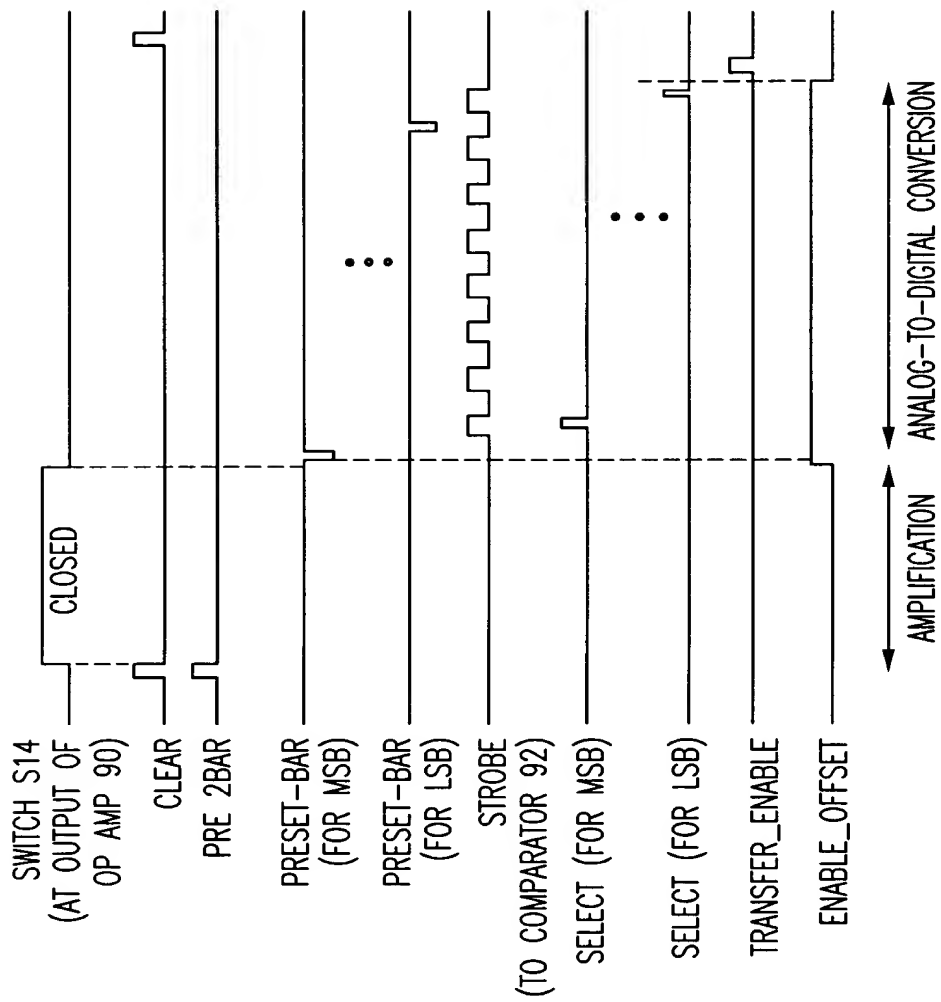
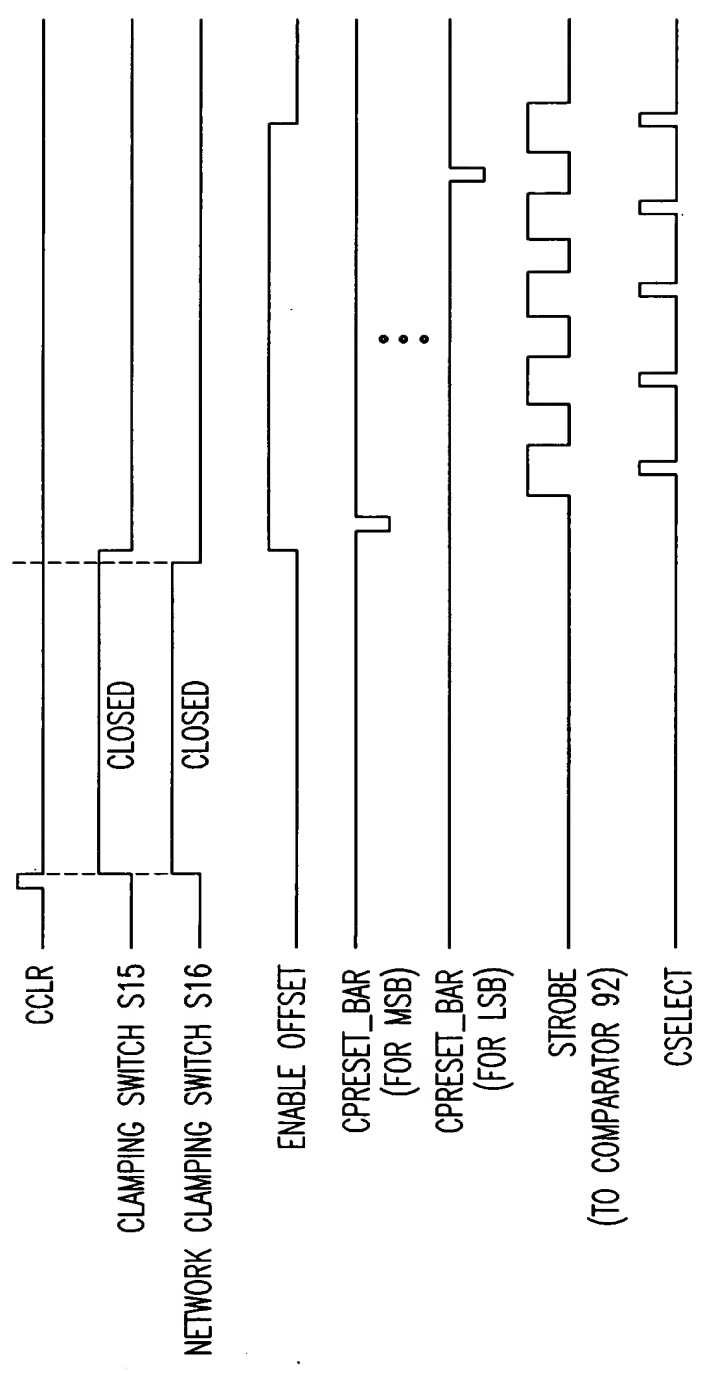
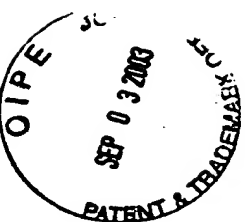


FIG. 11





CALIBRATION TIMING

FIG. 13

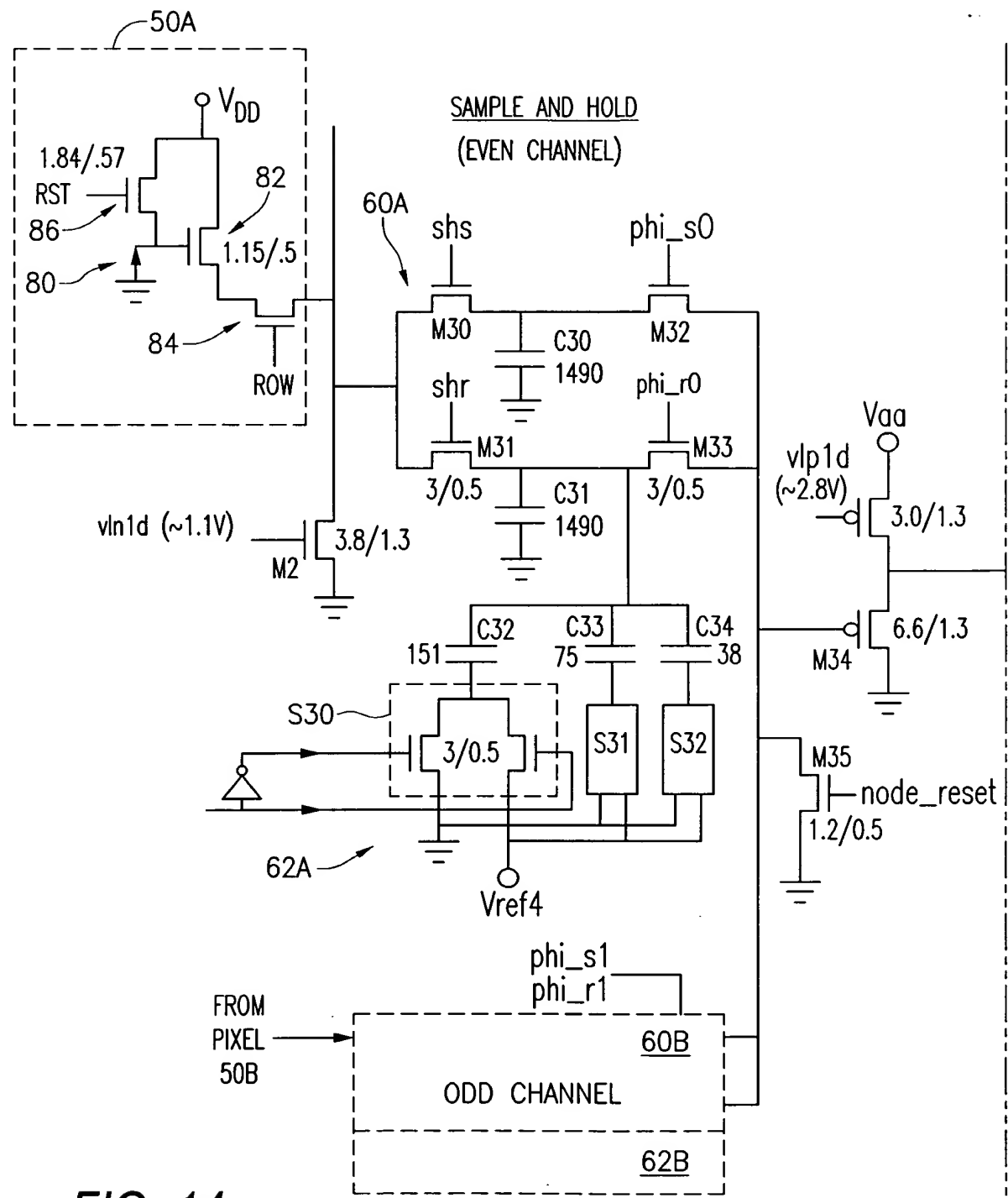


FIG. 14

FIG. 14A    FIG. 14B

FIG. 14A



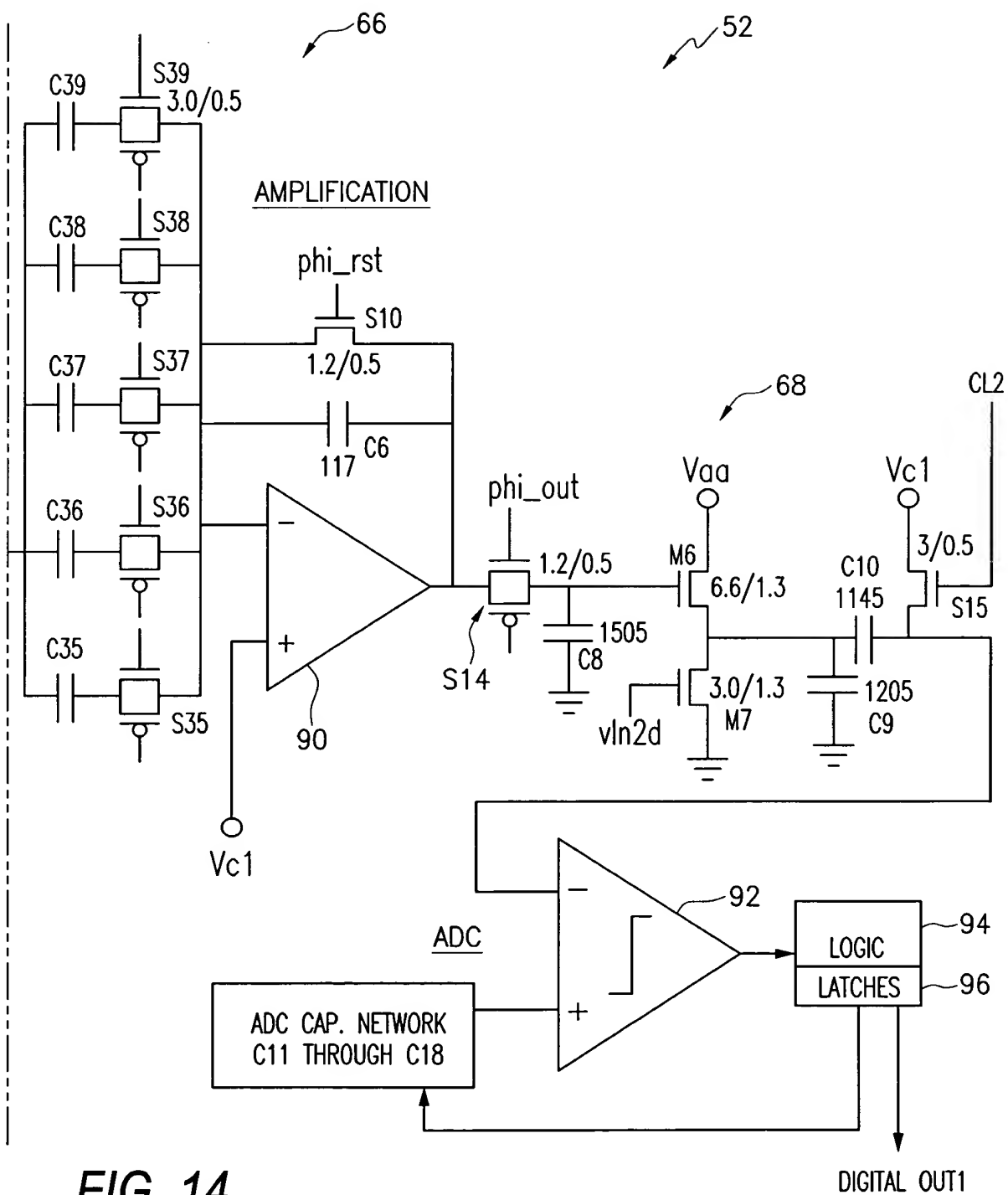


FIG. 14

FIG. 14A

FIG. 14B

FIG. 14B

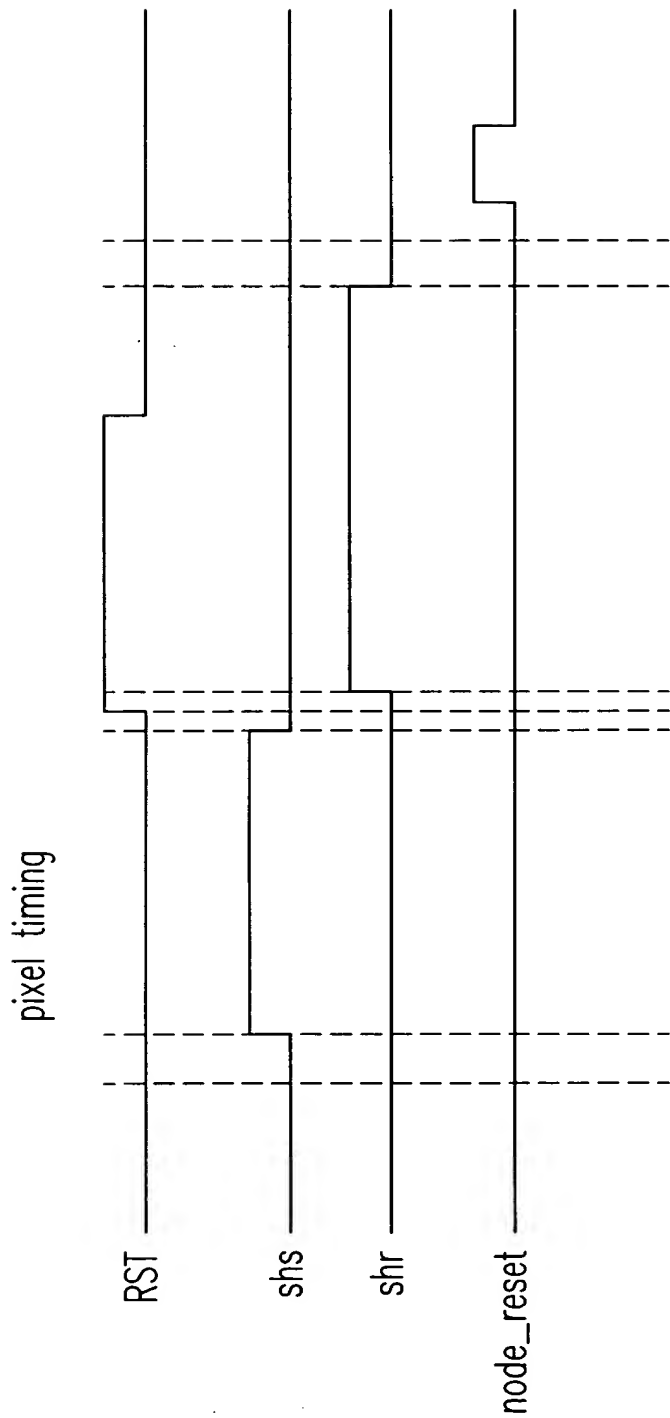
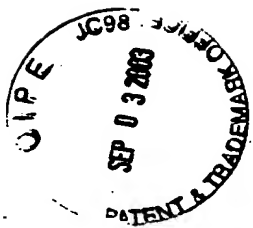


FIG. 15

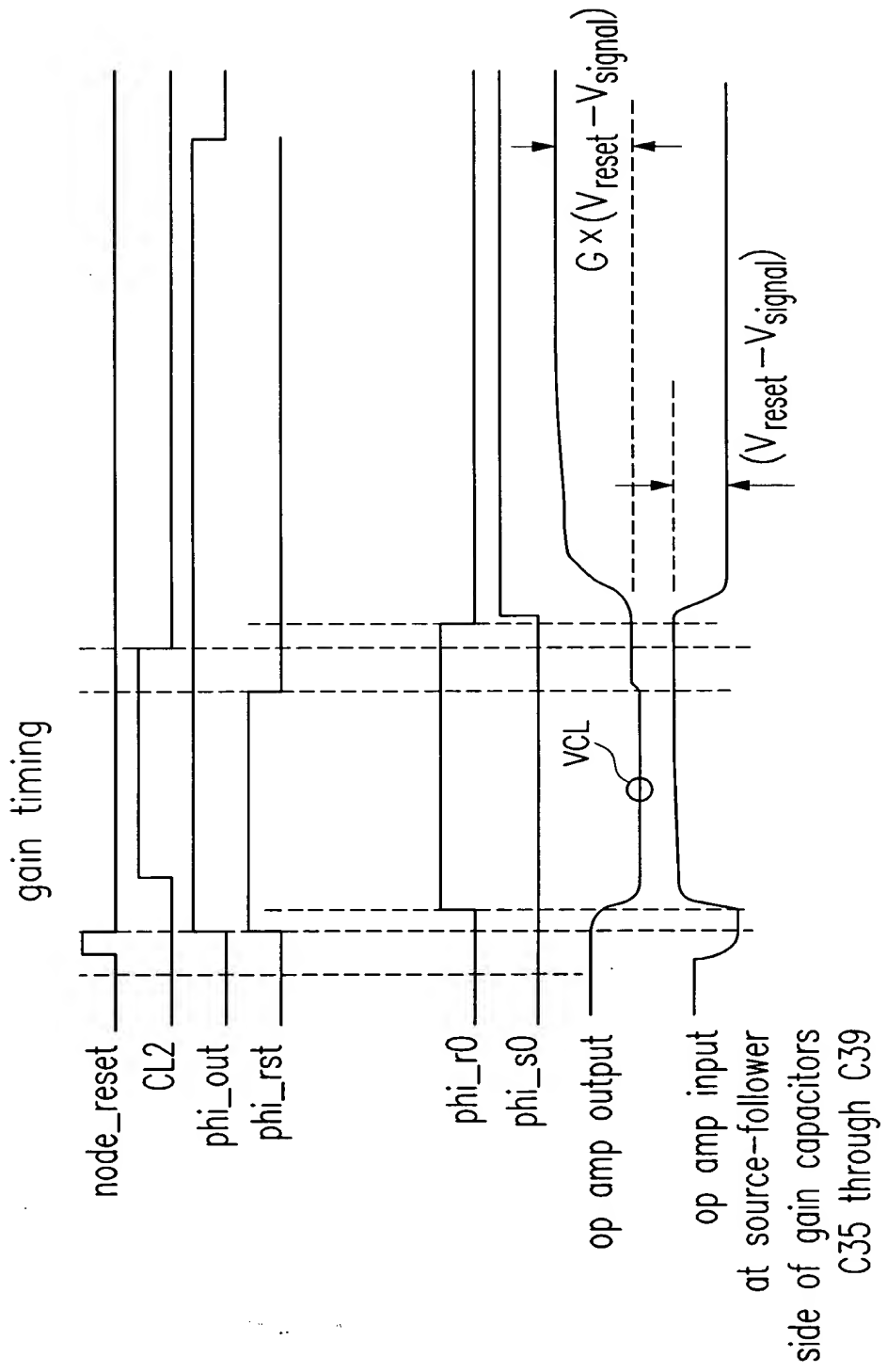


FIG. 16

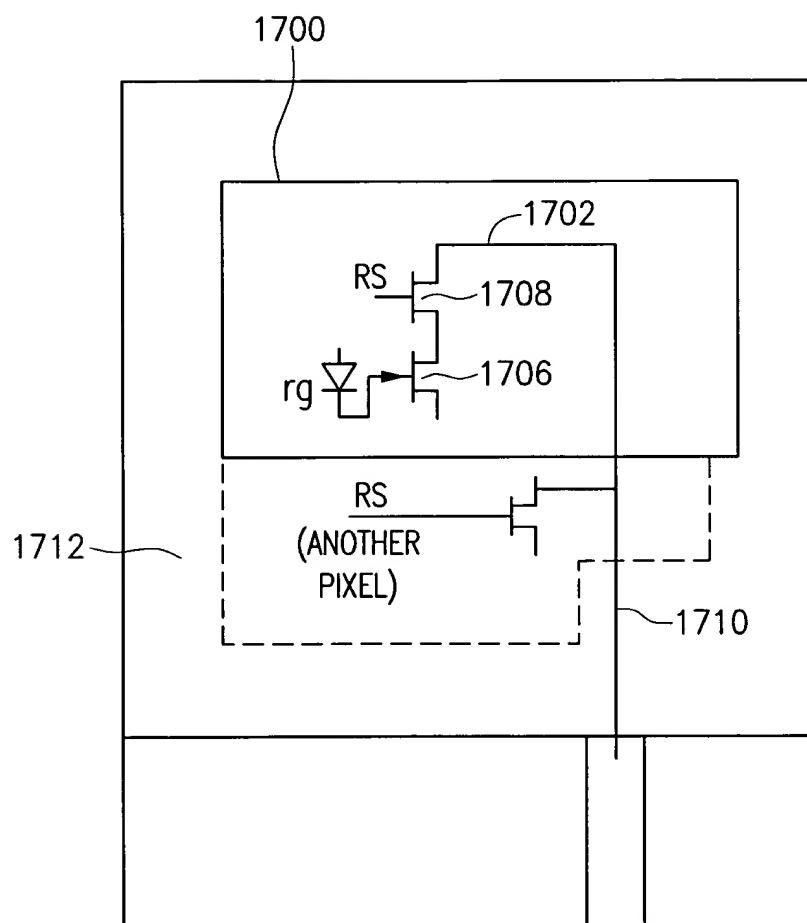
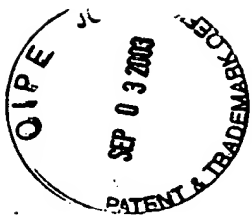


FIG. 17

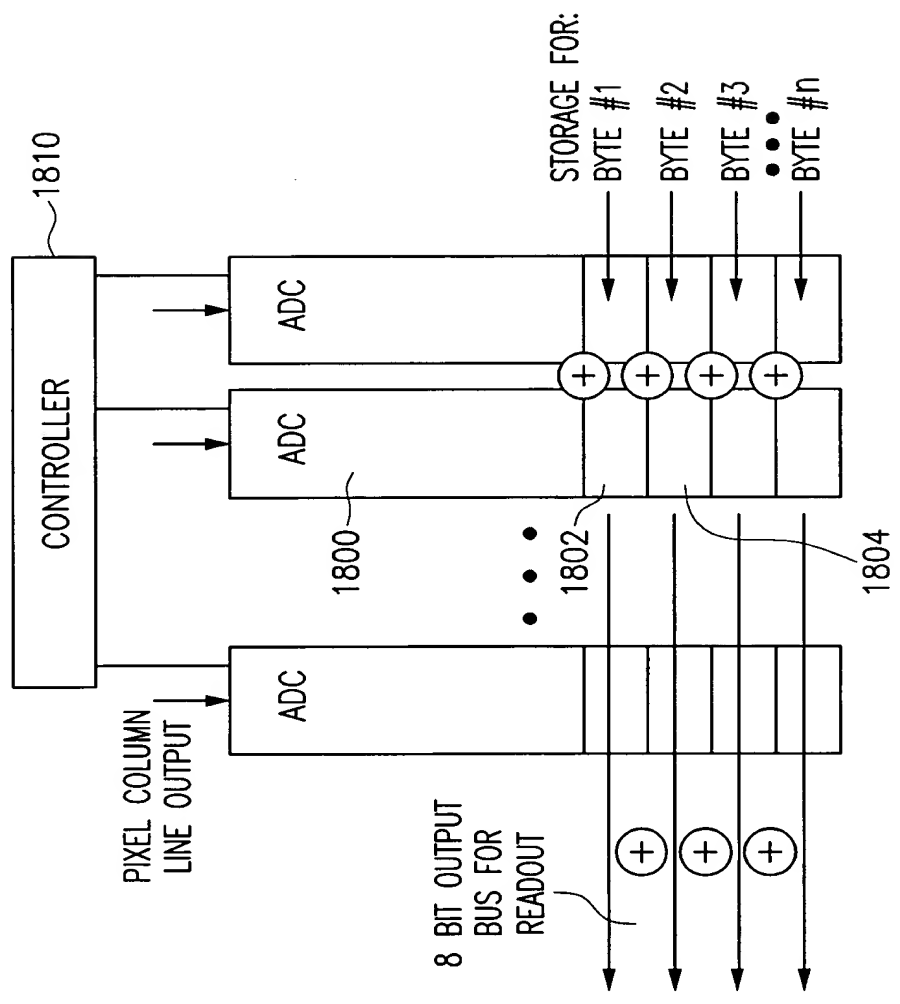


FIG. 18